

Fi 8 I

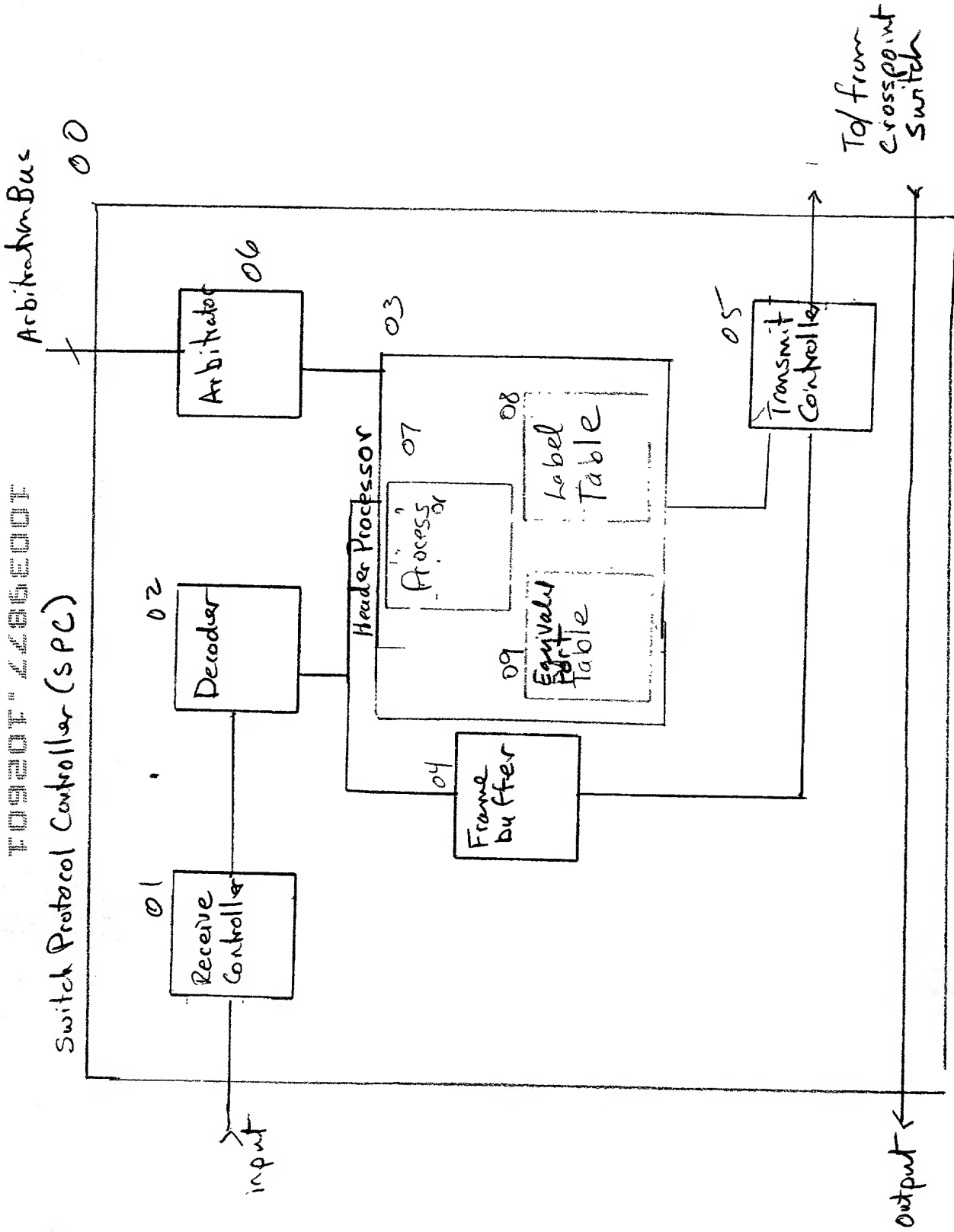


Fig 2

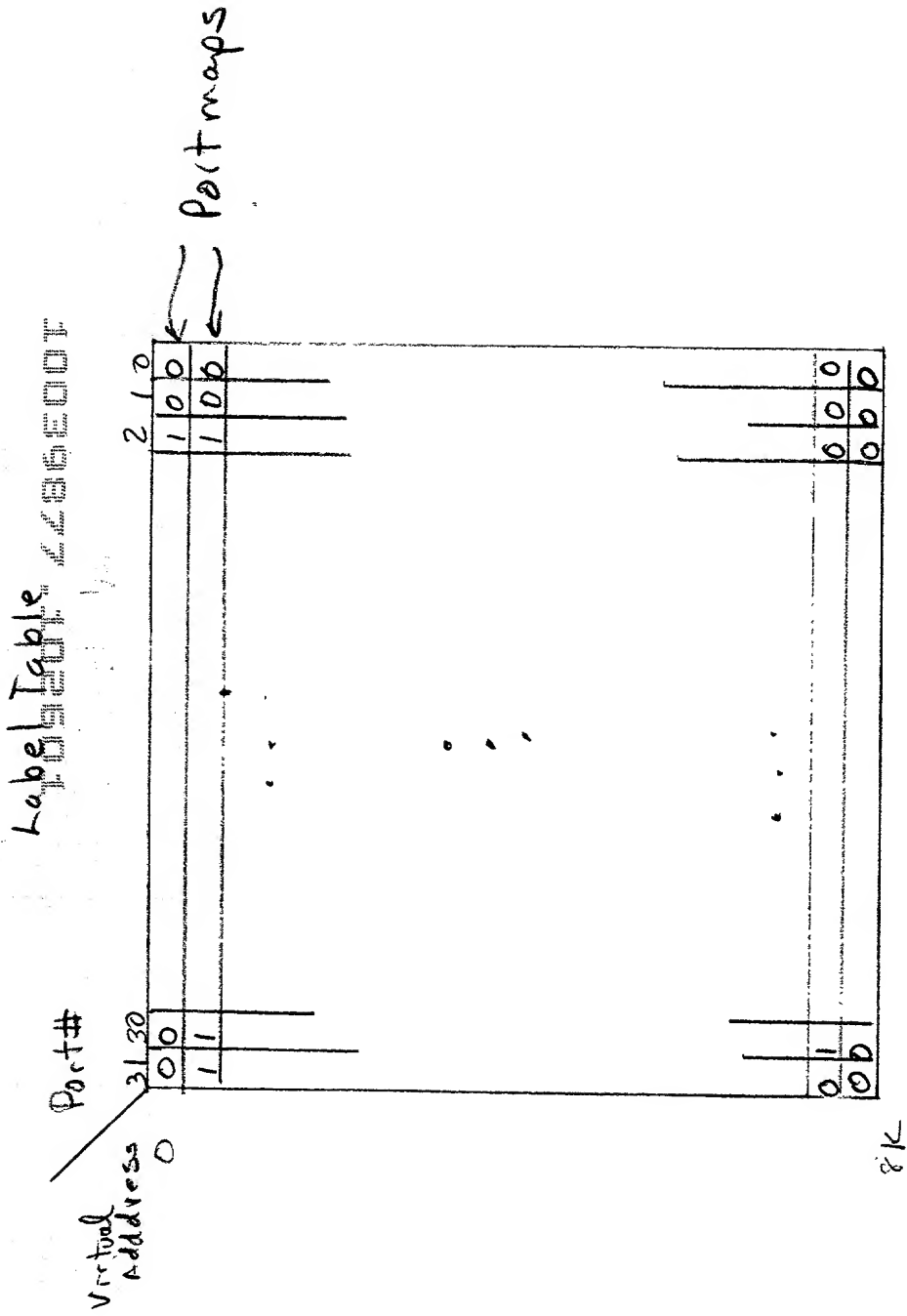


Fig 3

Frame

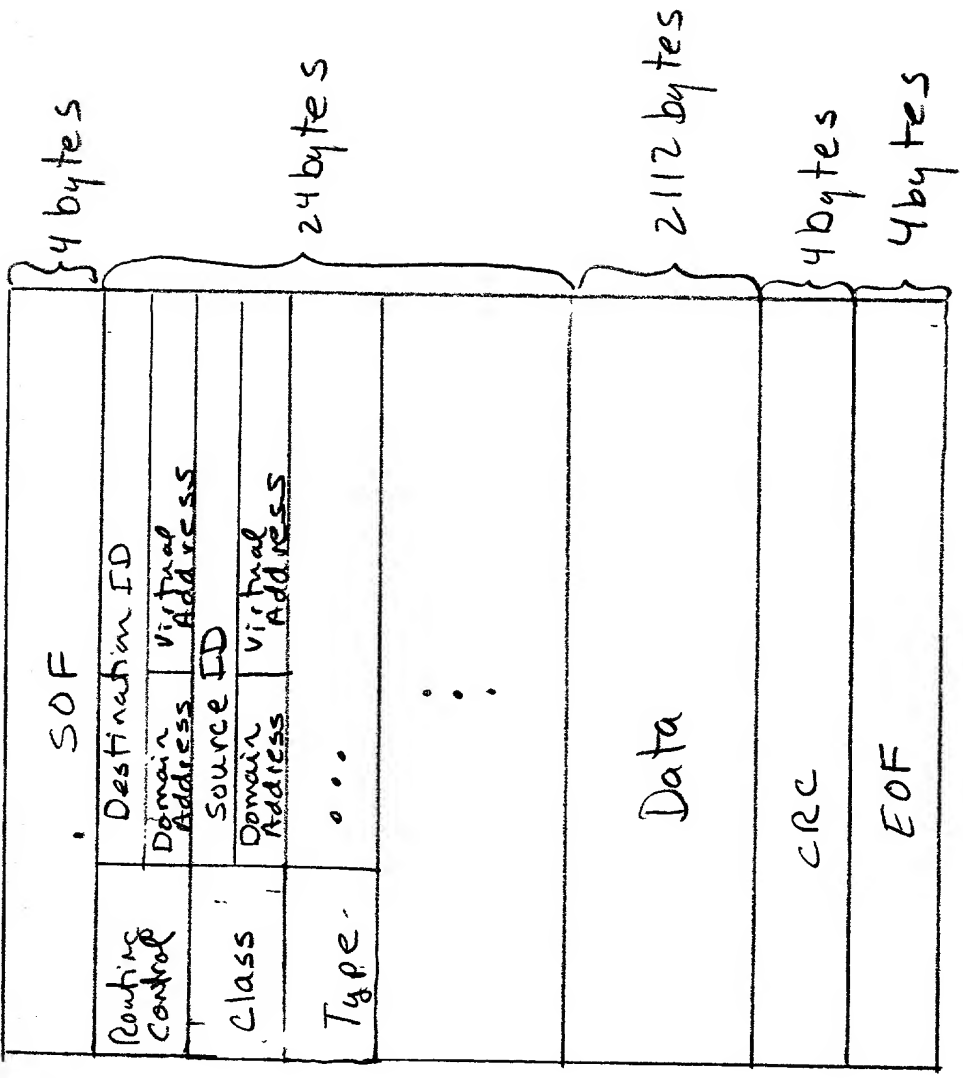


Fig 4

1039877-103601

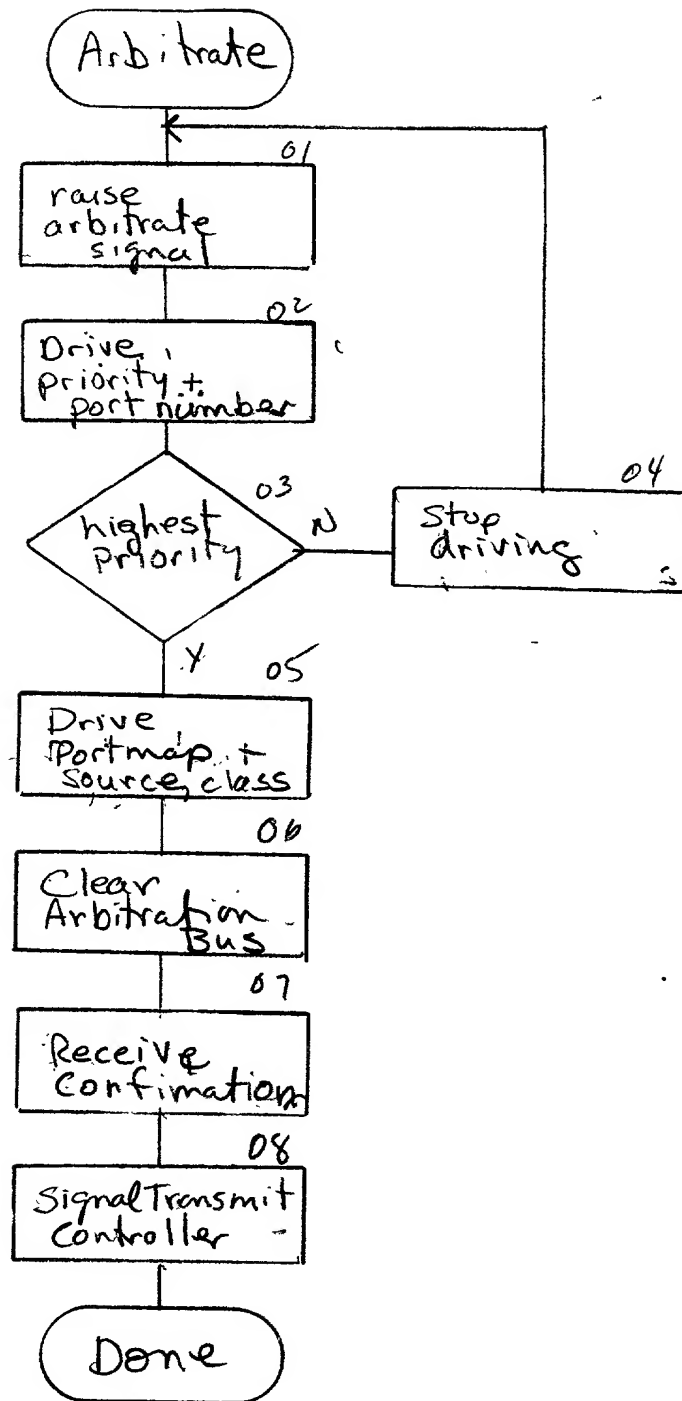


Fig 5

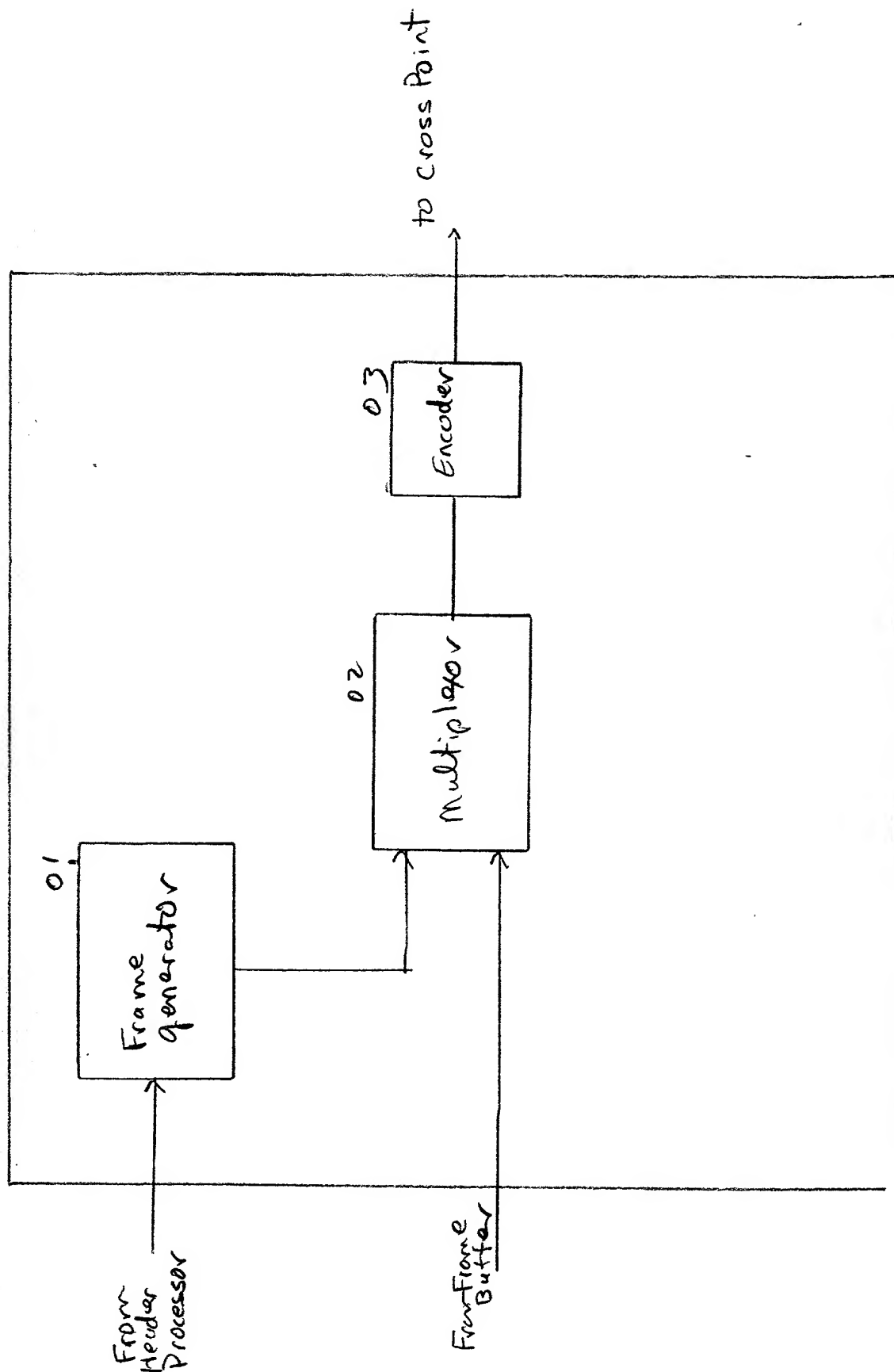


Fig 4

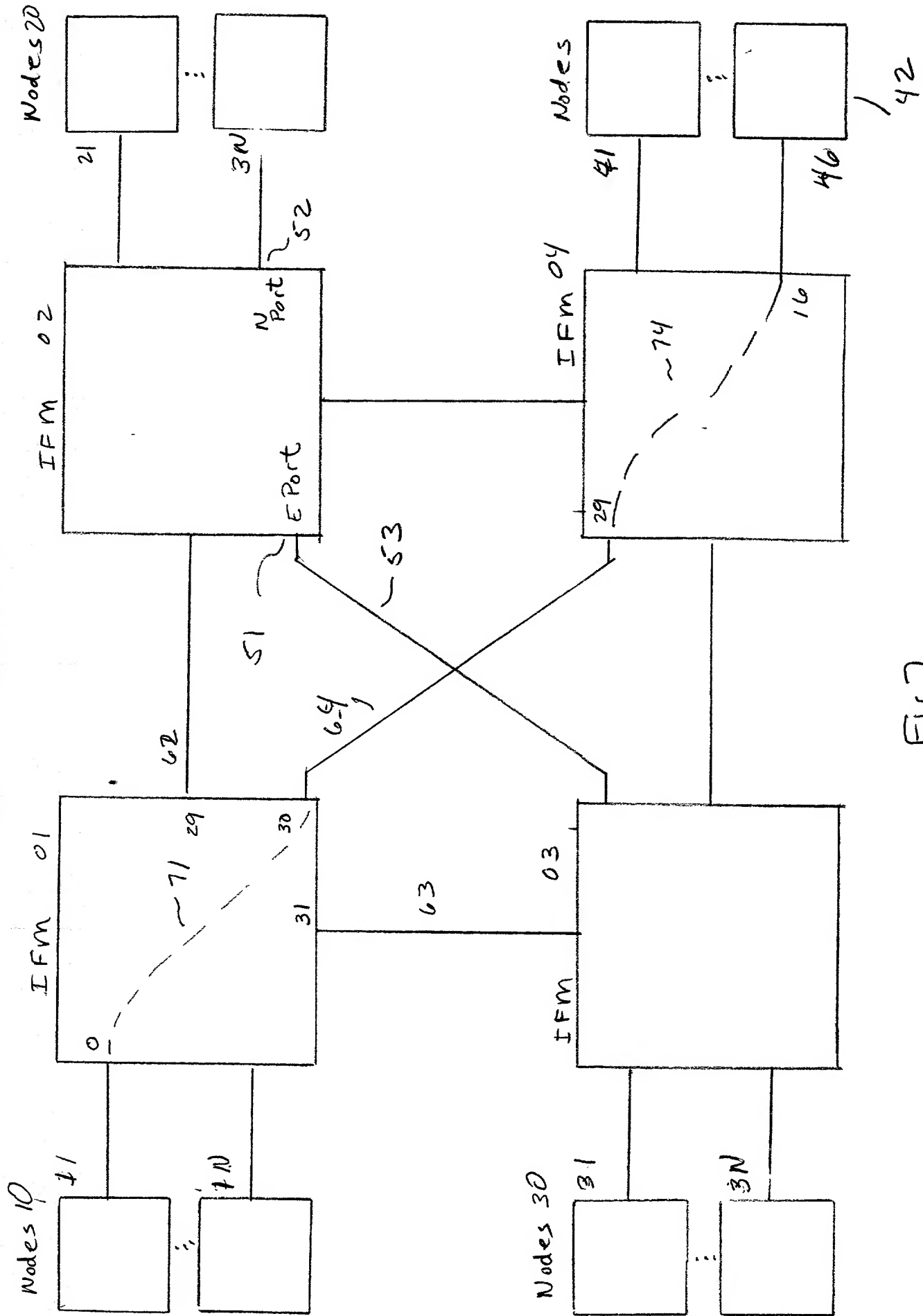


Fig 7

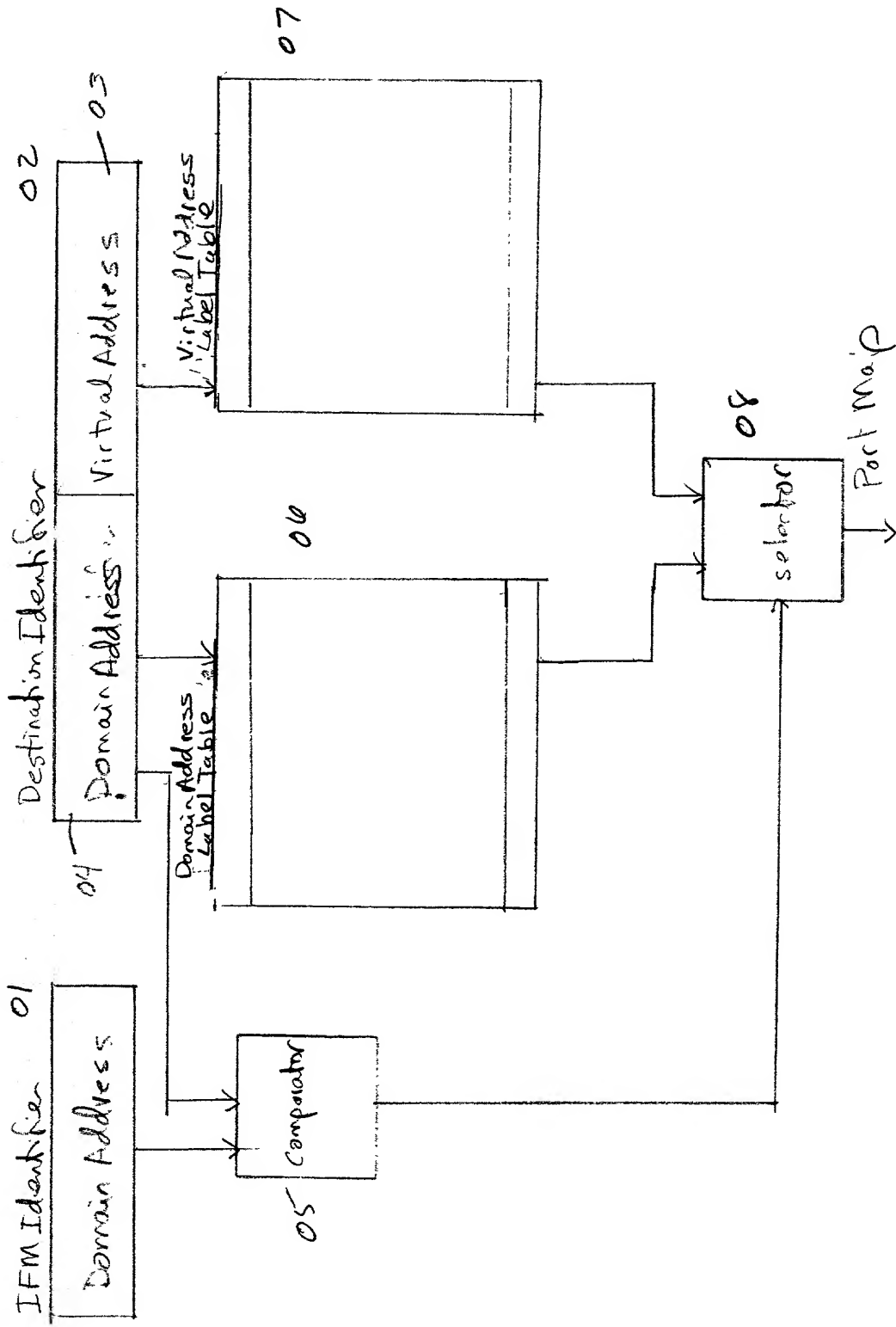


Fig. 8



# Quad Switch Protocol Controller

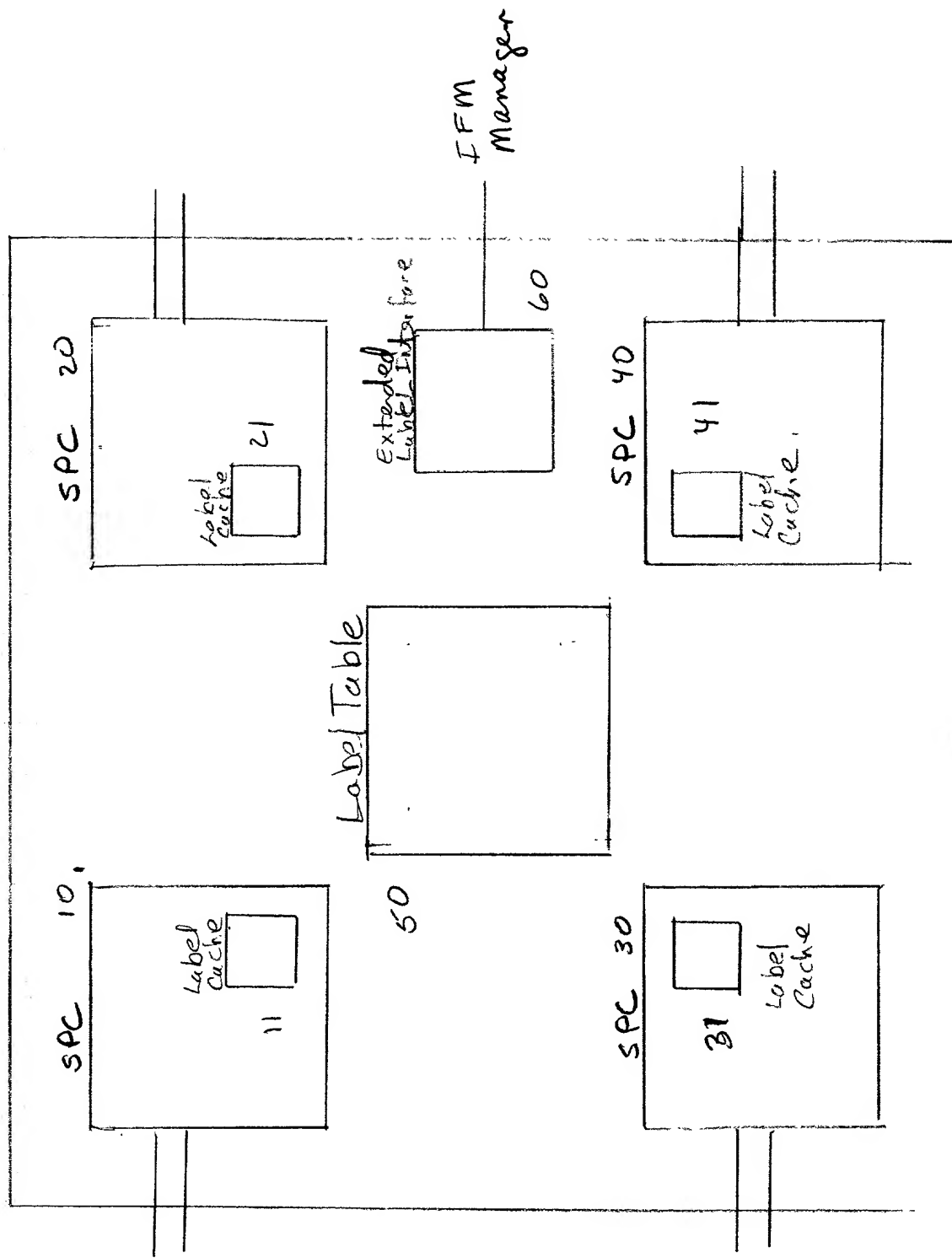


Fig. 9

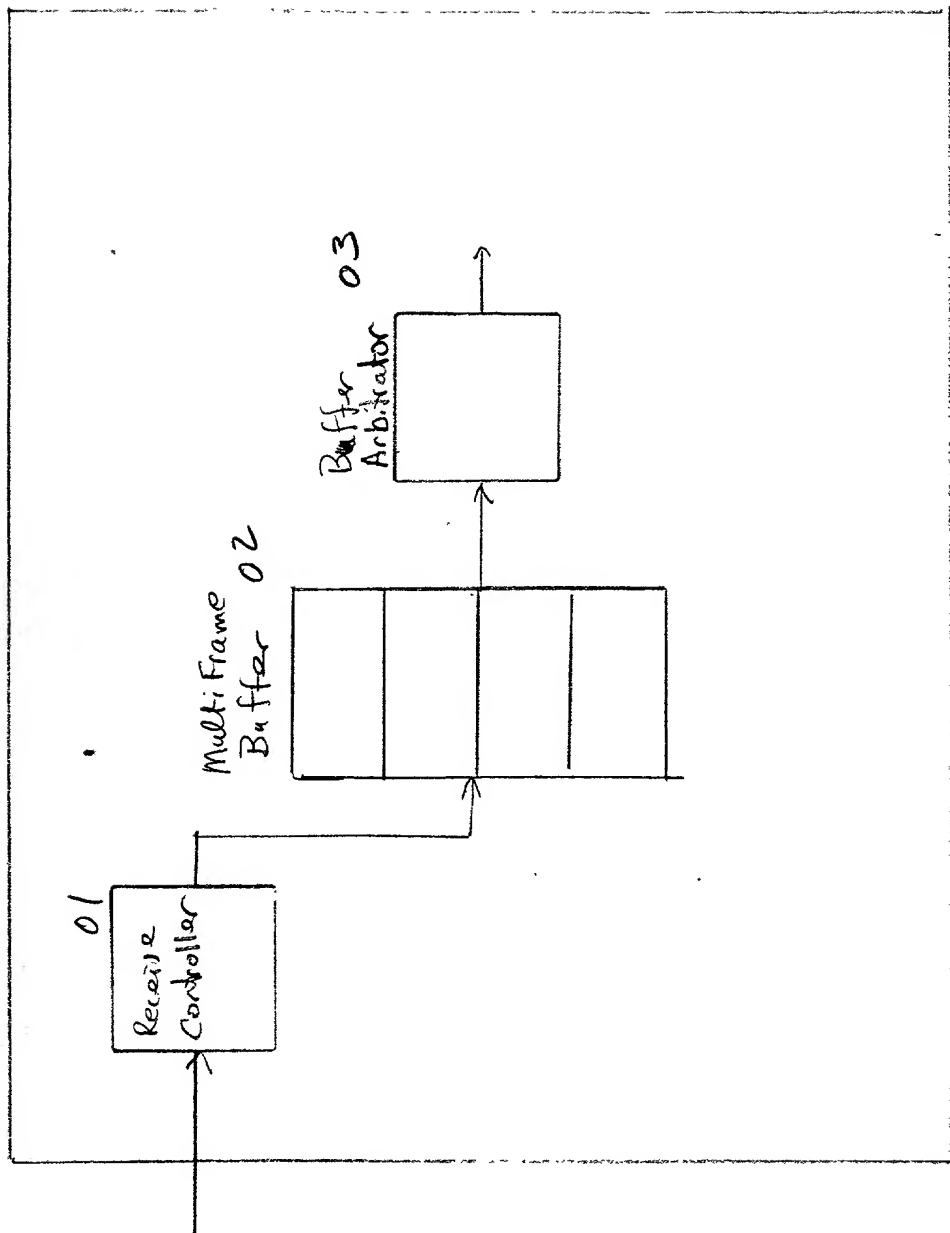


FIG 10

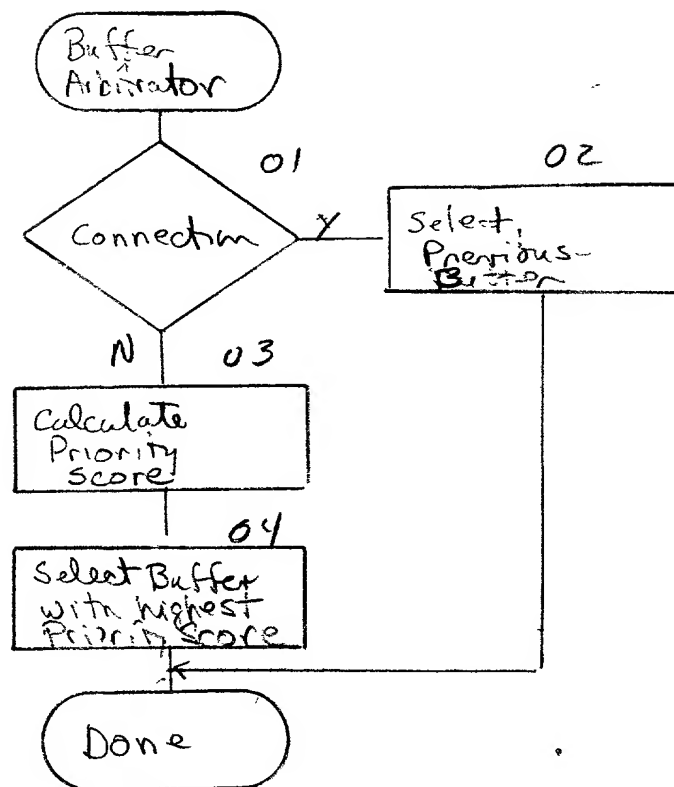


Fig 11

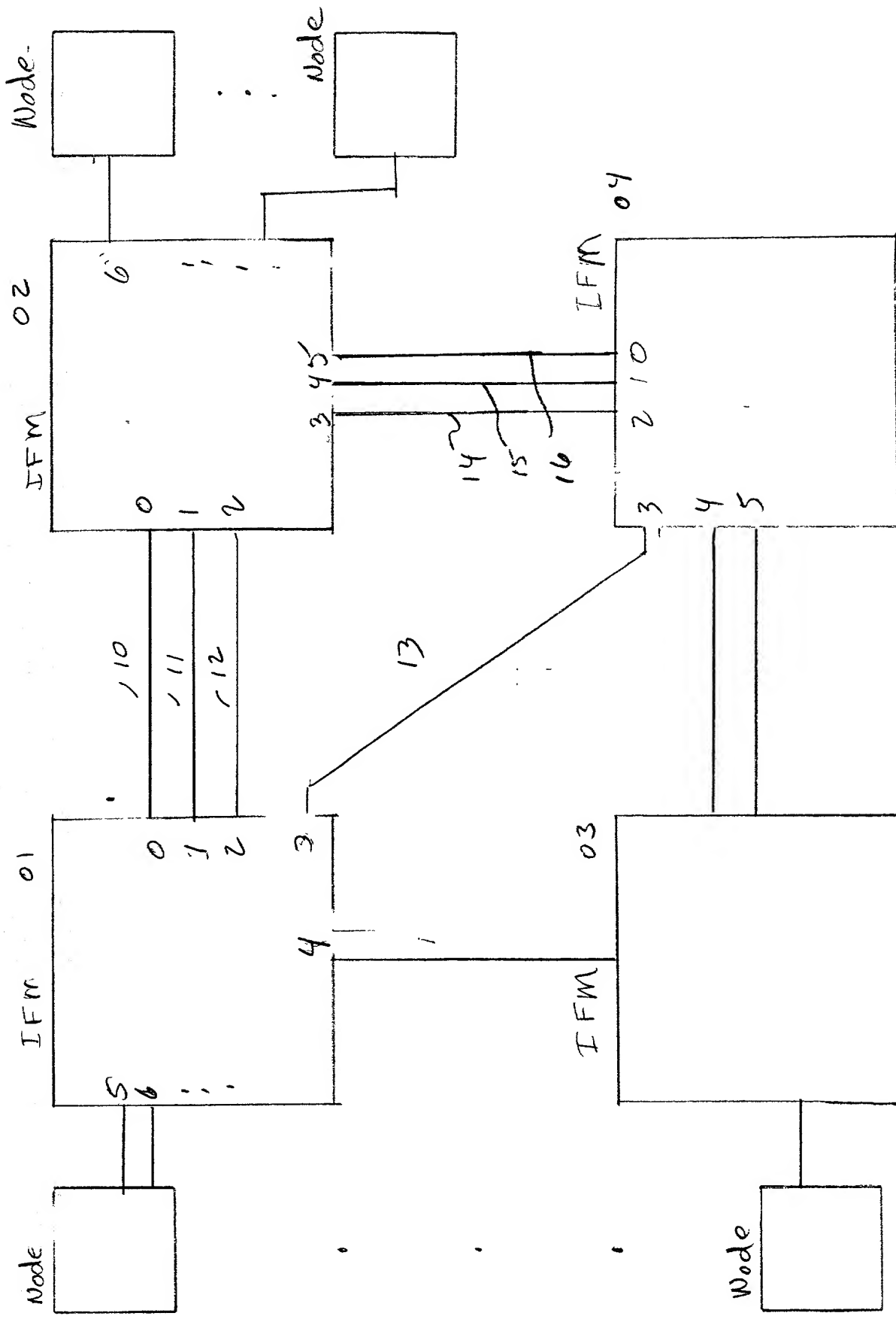


Fig 12

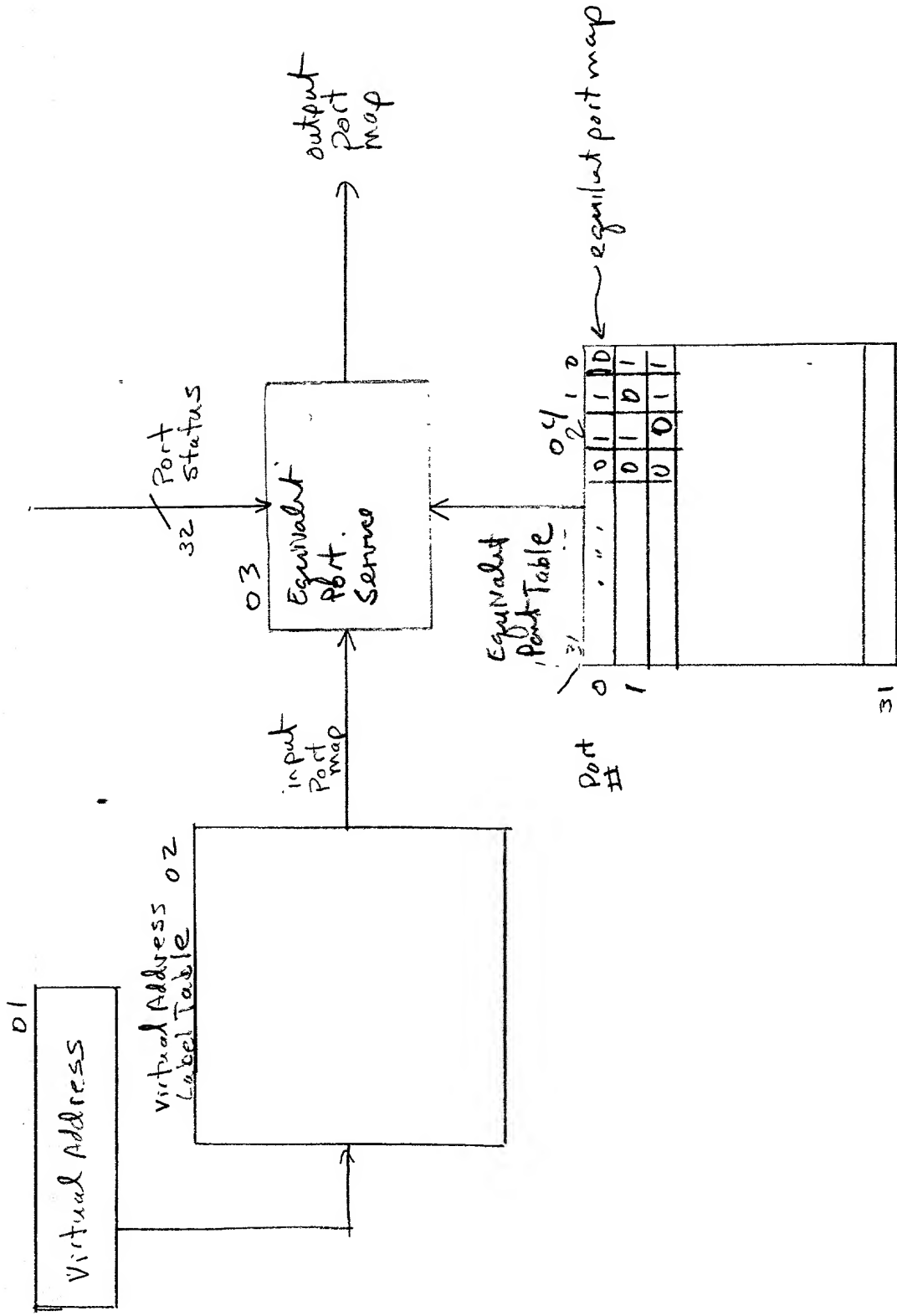


Fig 13

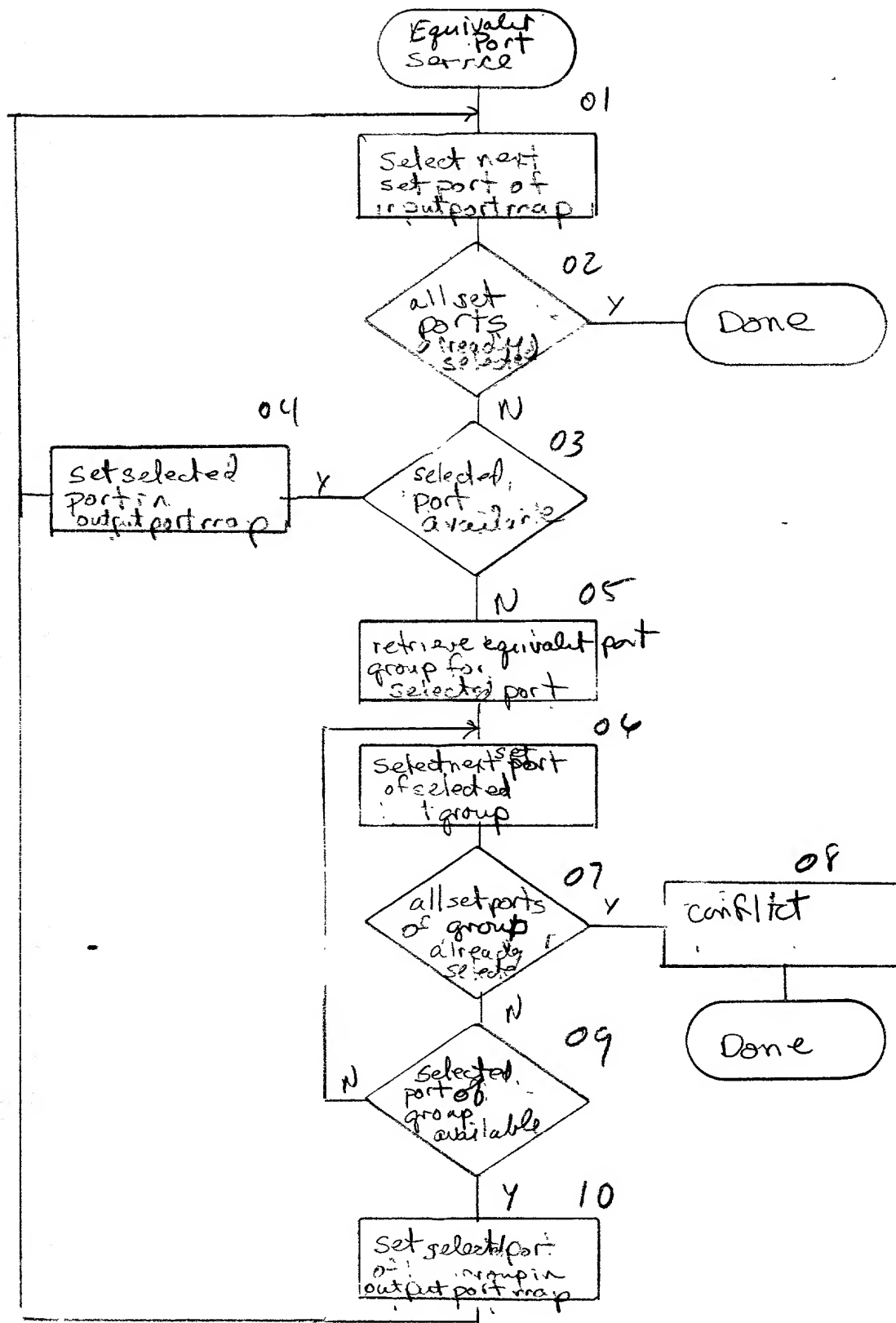


Fig 14

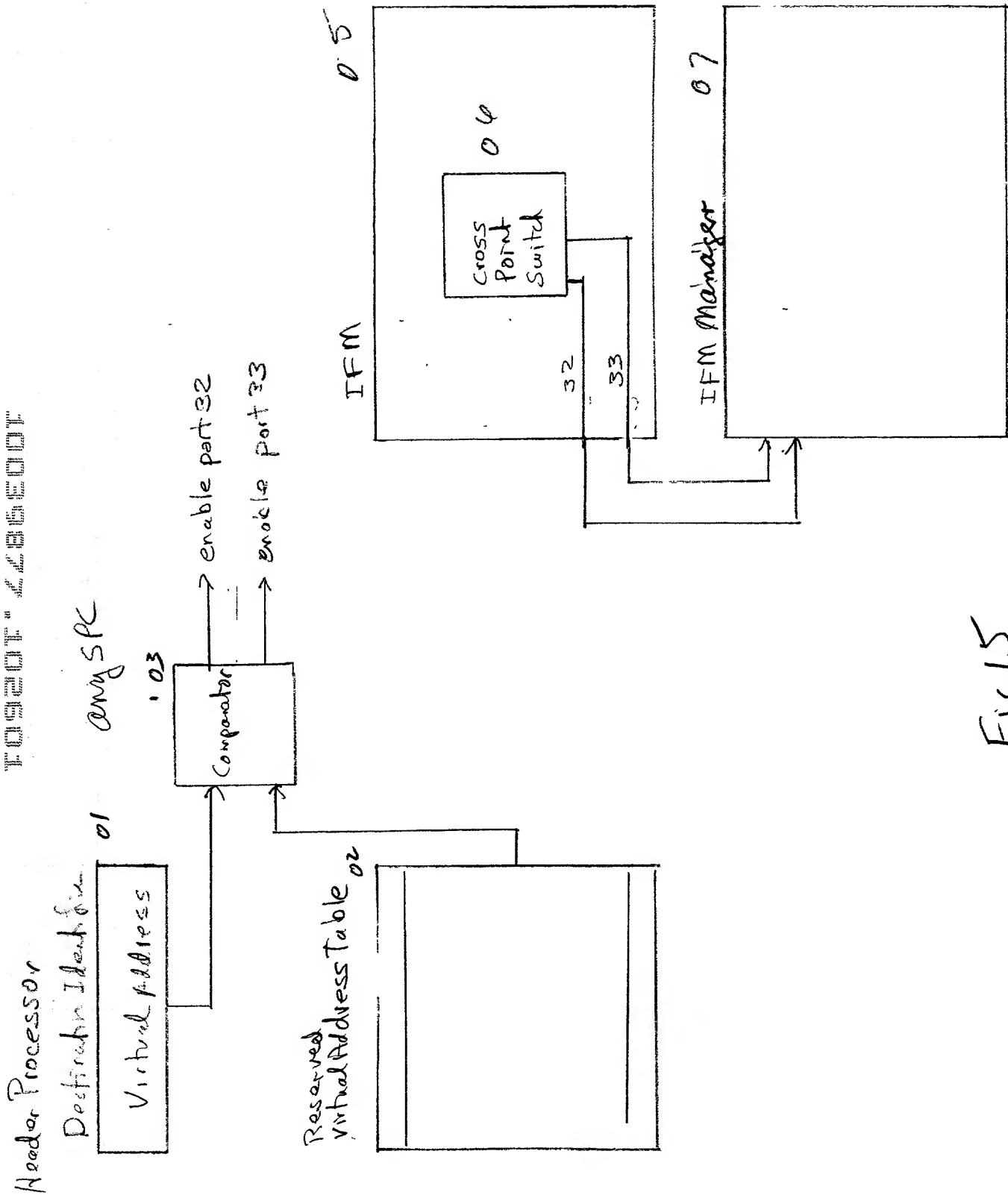
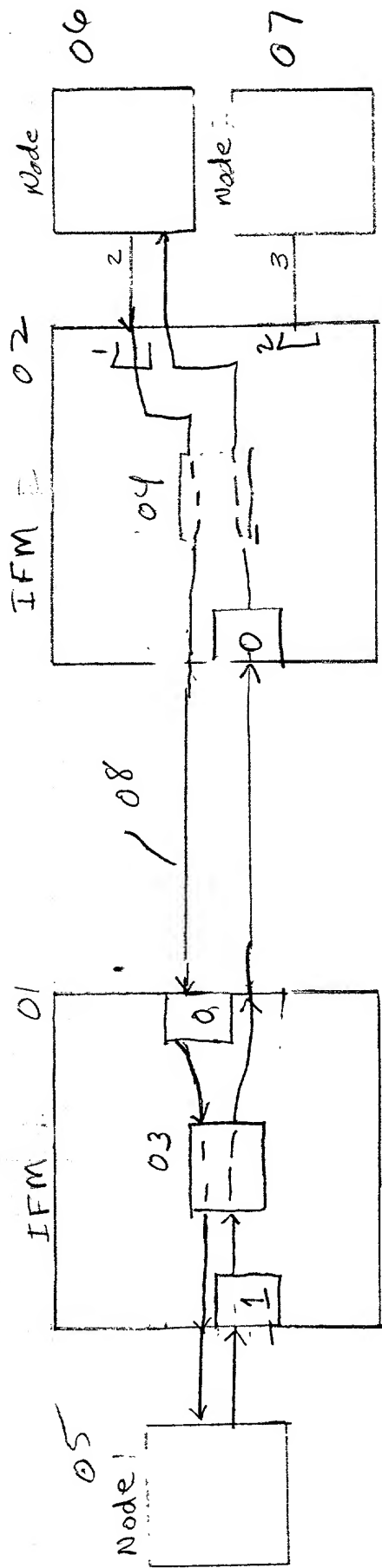


Fig 15



Dead lock

10

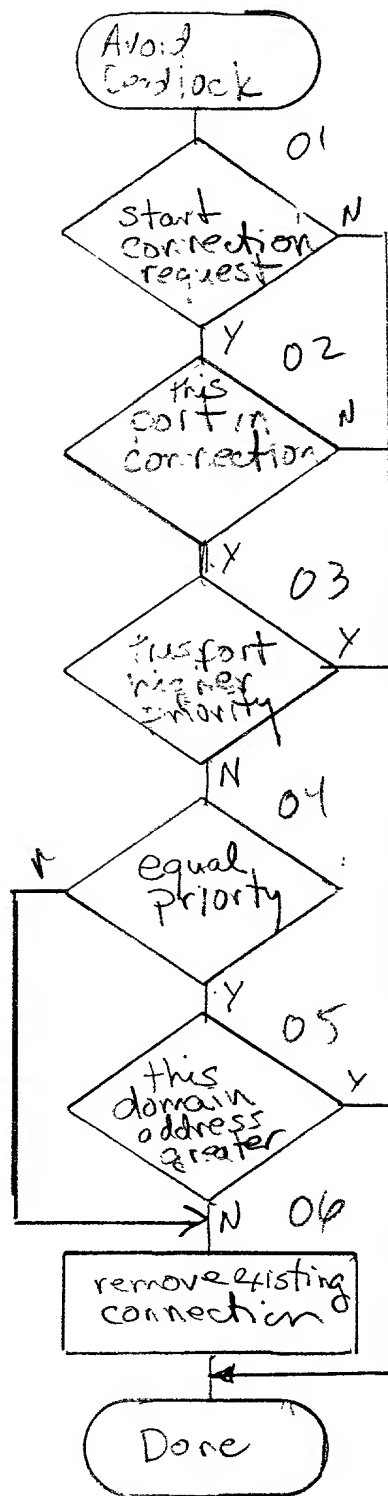
Time Node 1405 IFM 1601 Node 1606 IFM 1602

0 Send start connect 12

1	Connect 1 ↔ 0	Connect 2 ↔ 0
2	Forward start Connect	Forward start Connect
3	Can't forward Start connect Node 1	Can't forward Start connect

Fig 16





Water end-to-end  
not established

Fig 17

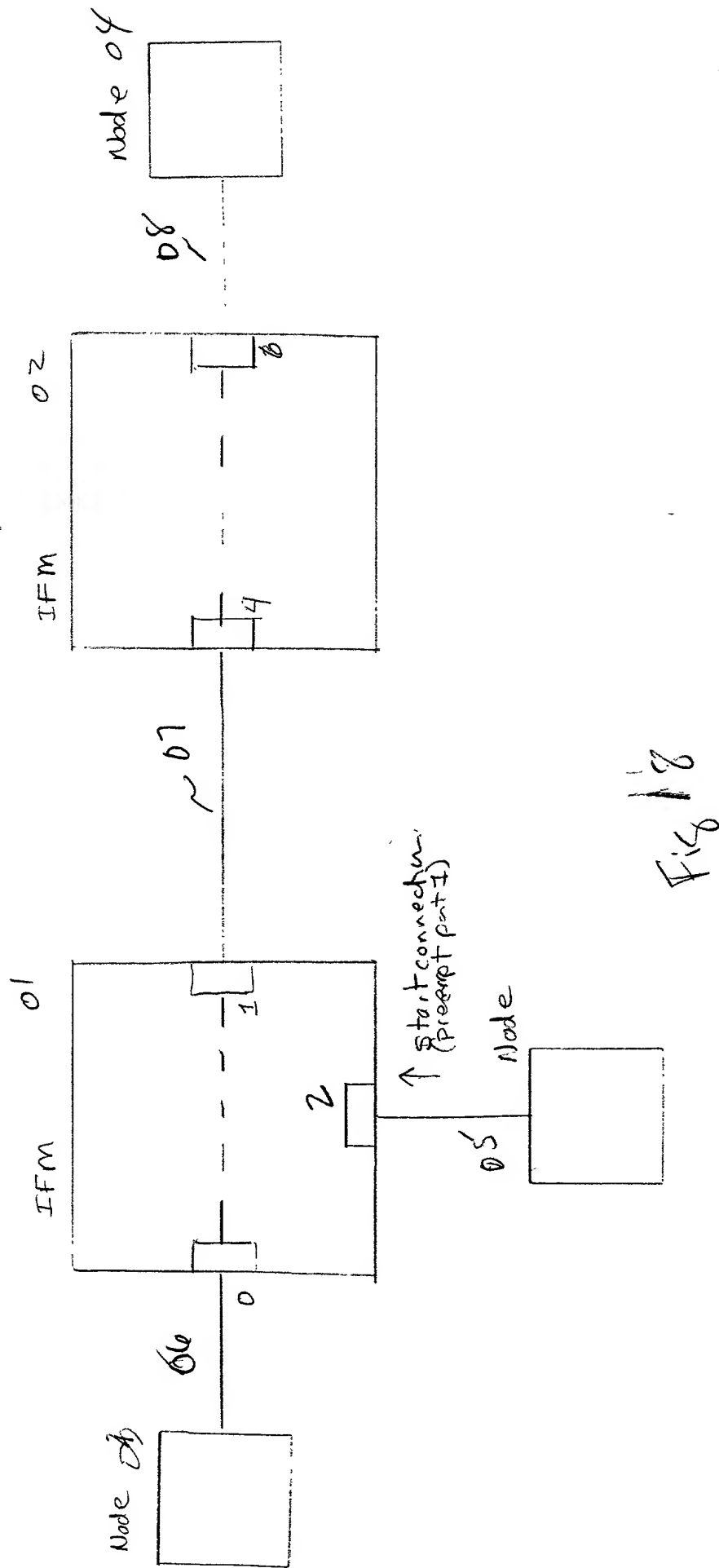


Fig 18

1003987-103601

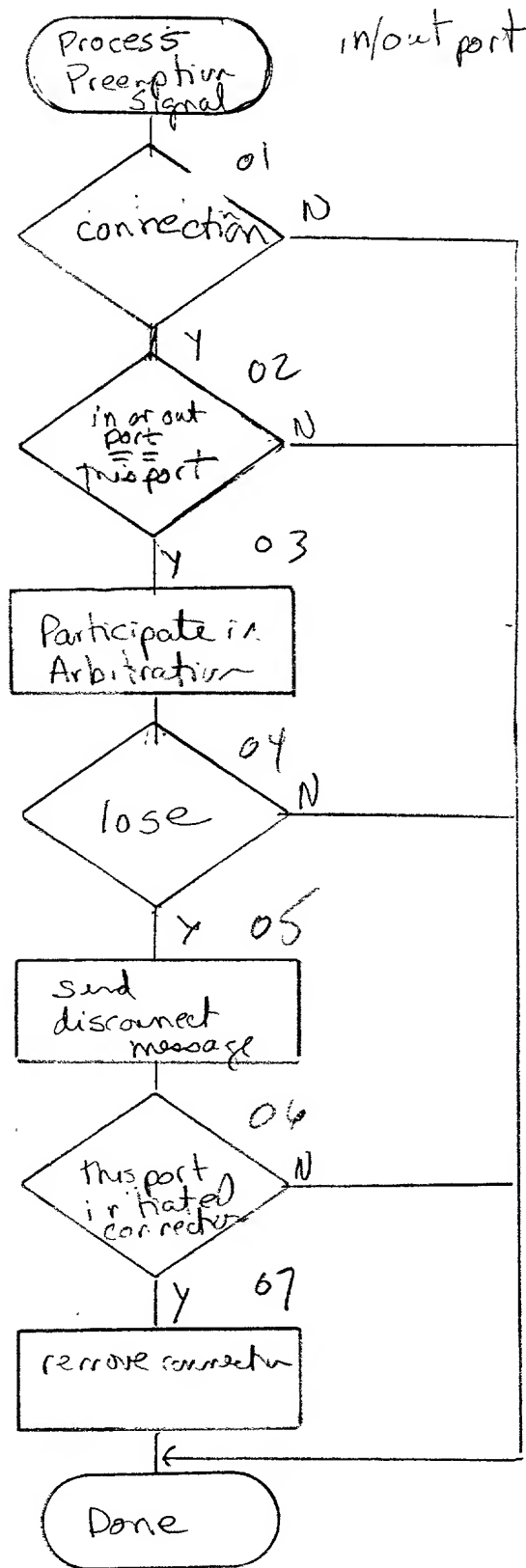


Fig. 19

1003987-10601

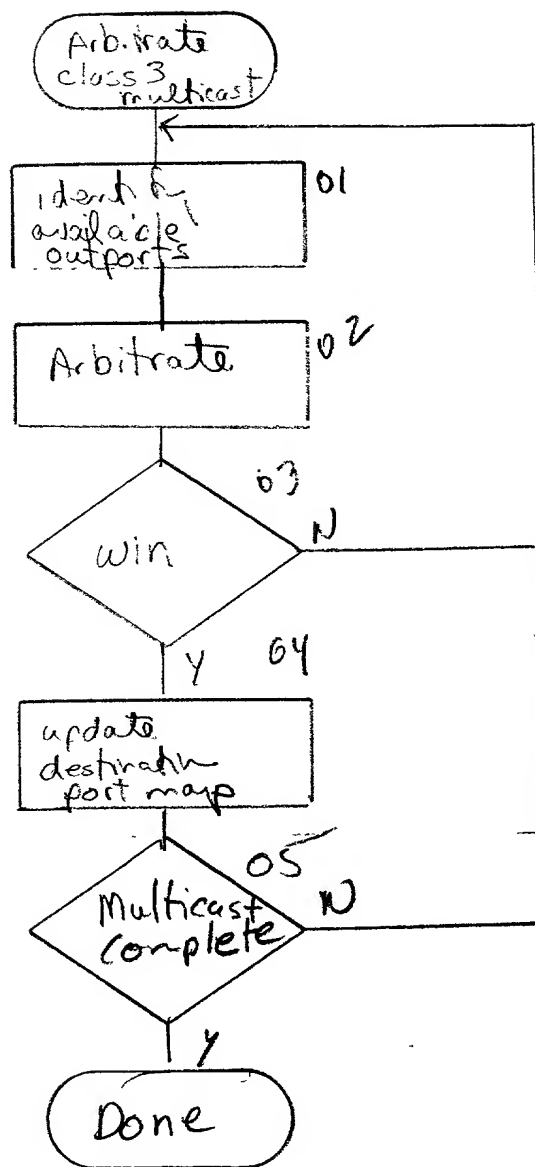


Figure 20